TOSHIBA GTR Module Silicon N Channel IGBT

MG100J7KS50

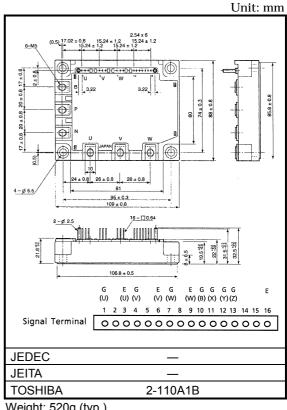
High Power Switching Applications Motor Control Applications

- The electrodes are isolated from case.
- High input impedance
- 7 IGBTs built into 1 package.
- Enhancement-mode
- High speed type IGBT

: VCE (sat) = 2.5 V (max) (@IC = 100 A)

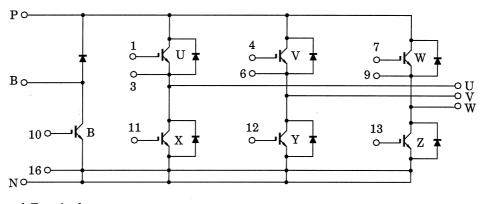
: $t_f = 0.5 \mu s \text{ (max) } (@I_C = 100 \text{ A})$

: $t_{rr} = 0.3 \mu s \text{ (max) (@I_F = 100 A)}$



Weight: 520g (typ.)

Equivalent Circuit



Signal Terminal

4:G(V)1:G(U)2 : Open 3 : E(U)7 : G(W)5 : Open $6 : \mathbf{E}(\mathbf{V})$ 8 : Open 10: G(B) 12 : G(Y)9 : E(W)11:G(X)13 : G(Z)14: Open 15 : Open 16 : E

1

2001-08-16

Inverter Stage

Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	Unit	
Collector-emitter voltage		V _{CES}	600	V	
Gate-emitter voltage		V _{GES}	±20	V	
Collector current	DC	I _C	100	А	
	1ms	I _{CP}	200	A	
Forward current	DC	I _F	100	Α	
	1ms	I _{FM}	200	^	
Collector power dissipation (Tc = 25°C)		PC	300	W	
Junction temperature		Tj	150	°C	
Storage temperature range		T _{stg}	-40 ~ 125	°C	
Isolation voltage		V _{Isol}	2500 (AC 1min.)	V	
Screw torque (Terminal / mounting)		_	3/3	N·m	

Electrical Characteristics (Ta = 25°C)

С	haracteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage c	urrent	I _{GES}	V _{GE} = ±20V, V _{CE} = 0V	_	_	±500	nA
Collector cut-of	f current	I _{CES}	V _{CE} = 600V, V _{GE} = 0V	1		1.0	mA
Gate-emitter cu	t-off voltage	V _{GE} (off)	V _{CE} = 5 V, I _C = 10 mA	5.0	_	8.0	V
Collector-emitte	er saturation voltage	V _{CE} (sat)	I _C = 100 A, V _{GE} = 15 V	_	2.0	2.5	V
Input capacitan	се	C _{ies}	V _{CE} = 10 V, V _{GE} = 0 V, f = 1 MHz	_	8.5	_	nF
Forward voltage	rard voltage $V_F = I_F = 100 \text{ A}$		I _F = 100 A	_	2.3	3.0	V
Switching time	Rise time	t _r	Inductive load $V_{CC} = 300 \text{ V}$ $I_C = 100 \text{ A}$ $V_{GE} = \pm 15 \text{ V}$ $R_G = 13 \Omega$ (Note 1)	_	0.12	0.24	μs
	Turn-on time	t _{on}		_	0.45	0.90	
	Fall time	t _f		_	0.20	0.50	
	Turn-off time	t _{off}		_	0.50	1.00	
	Reverse recovery time	trr		_	0.10	0.30	
Thermal resistance		R _{th (j-c)}	Transistor stage	_	_	0.42	
		R _{th (c-f)}	Diode stage	_	_	1.00	°C/W
			Case to fin (Note 2)	_	0.05	_	

Note 2: Silicone grease is applied.

2

Brake Stage

Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	Unit	
Collector-emitter voltage		V _{CES}	600	V	
Gate-emitter voltage		V _{GES}	±20	V	
Reverse voltage		V _R	600	V	
Collector ourrent	DC	IC	50	А	
Collector current Forward current	1ms	I _{CP}	100	A	
	DC	l _F	50	А	
Forward current	1ms	I _{FM}	±20 600 50 100 50 100 80 150 -40 ~ 125	A	
Collector power dissipation (Tc = 25°C)		Pc	80	W	
Junction temperature		Tj	150	°C	
Storage temperature range		T _{stg}	-40 ~ 125	°C	
Isolation voltage	on voltage			V	
Screw torque (Terminal / mo	crew torque (Terminal / mounting)		3/3	N·m	

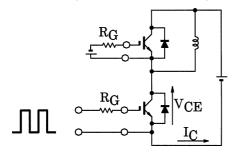
Electrical Characteristics (Ta = 25°C)

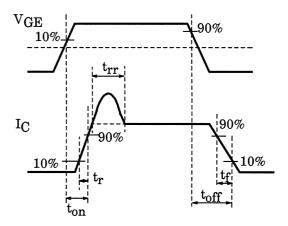
Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage c	urrent	I _{GES}	V _{GE} = ±20 V, V _{CE} = 0 V	_	_	±500	nA
Collector cut-of	f current	I _{CES}	V _{CE} = 600 V, V _{GE} = 0 V	_	_	1.0	mA
Gate-emitter cu	t-off voltage	V _{GE} (off)	V _{CE} = 5 V, I _C = 5 mA	5.0	_	8.0	V
Collector-emitte	er saturation voltage	V _{CE (sat)}	I _C = 50 A, V _{GE} = 15 V	_	2.0	2.5	V
Input capacitan	ce	C _{ies}	V _{CE} = 10 V, V _{GE} = 0 V, f = 1 MHz	-	4.0	_	nF
Reverse curren	t	I _R	V _R = 600 V	-	_	1.0	mA
Forward voltage	vard voltage		I _F = 50 A	-	2.2	2.8	V
	Rise time	t _r	Inductive-load	_	0.08	0.16	
Switching time	Turn-on time	t _{on}	V _{CC} = 300 V	-	0.10	0.20	
	Fall time	t _f	$I_C = 50 \text{ A}$ $V_{GE} = \pm 15 \text{ V}$ $R_G = 24 \Omega$	-	0.22	0.44	μs
	Turn-off time	t _{off}		-	0.50	1.00	
	Reverserecovery time	trr	(Note 1)	_	0.23	0.35	
	,		Transistor stage		_	1.56	
Thermal resistance		R _{th (c-f)}	Diode stage	_	_	2.00	°C/W
			Case to fin (Note 2)	_	0.05	_	

3

Note 2: Silicone grease is applied.

Note 1: Switching time test circuit & timing chart





4

RESTRICTIONS ON PRODUCT USE

000707EAA

- TOSHIBA is continually working to improve the quality and reliability of its products. Nevertheless, semiconductor devices in general can malfunction or fail due to their inherent electrical sensitivity and vulnerability to physical stress. It is the responsibility of the buyer, when utilizing TOSHIBA products, to comply with the standards of safety in making a safe design for the entire system, and to avoid situations in which a malfunction or failure of such TOSHIBA products could cause loss of human life, bodily injury or damage to property.
 In developing your designs, please ensure that TOSHIBA products are used within specified operating ranges as set forth in the most recent TOSHIBA products specifications. Also, please keep in mind the precautions and conditions set forth in the "Handling Guide for Semiconductor Devices," or "TOSHIBA Semiconductor Reliability Handbook" etc..
- The TOSHIBA products listed in this document are intended for usage in general electronics applications (computer, personal equipment, office equipment, measuring equipment, industrial robotics, domestic appliances, etc.). These TOSHIBA products are neither intended nor warranted for usage in equipment that requires extraordinarily high quality and/or reliability or a malfunction or failure of which may cause loss of human life or bodily injury ("Unintended Usage"). Unintended Usage include atomic energy control instruments, airplane or spaceship instruments, transportation instruments, traffic signal instruments, combustion control instruments, medical instruments, all types of safety devices, etc.. Unintended Usage of TOSHIBA products listed in this document shall be made at the customer's own risk.
- The information contained herein is presented only as a guide for the applications of our products. No
 responsibility is assumed by TOSHIBA CORPORATION for any infringements of intellectual property or other
 rights of the third parties which may result from its use. No license is granted by implication or otherwise under
 any intellectual property or other rights of TOSHIBA CORPORATION or others.
- The information contained herein is subject to change without notice.